

FIG. 1

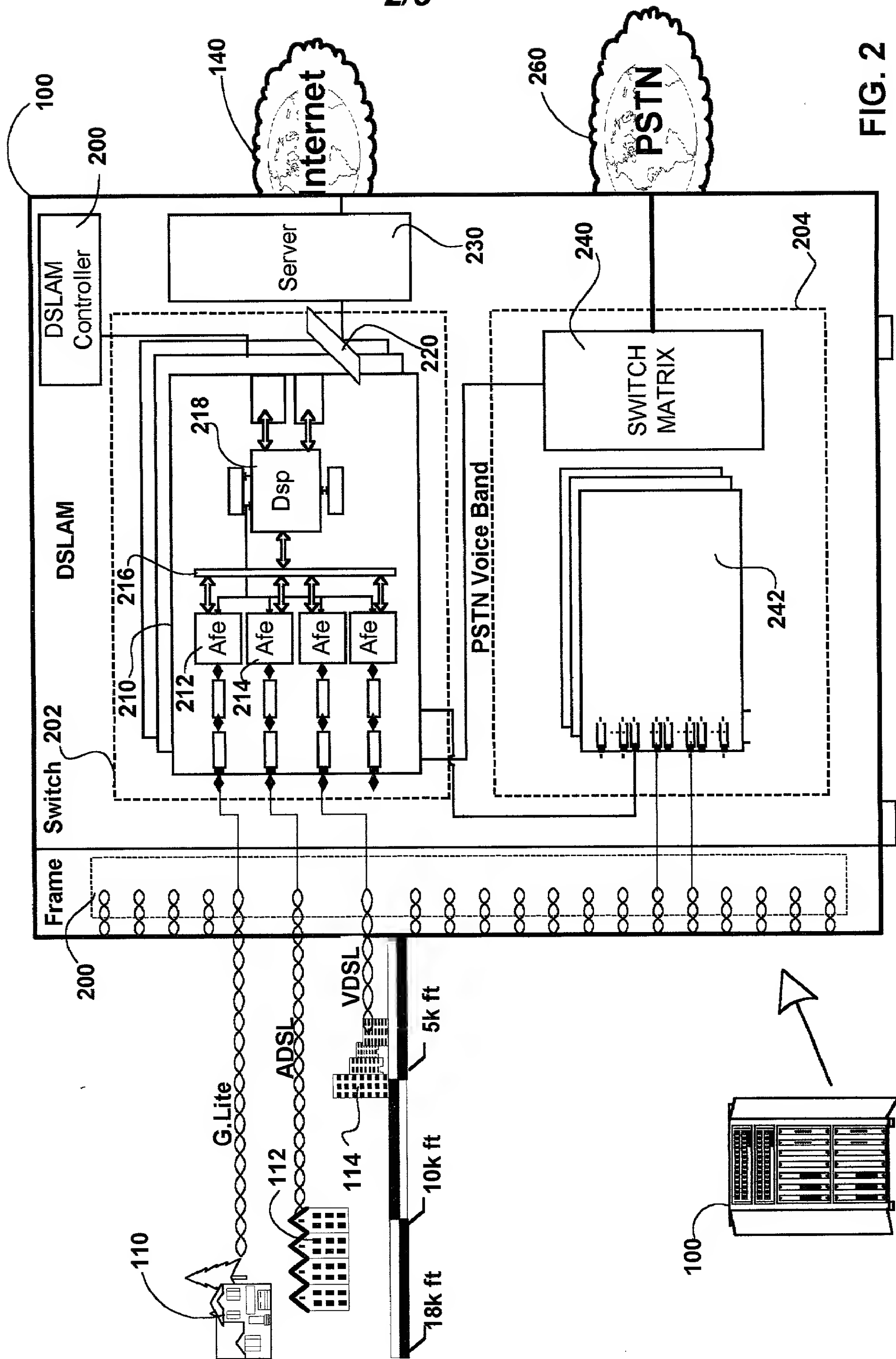


FIG. 2

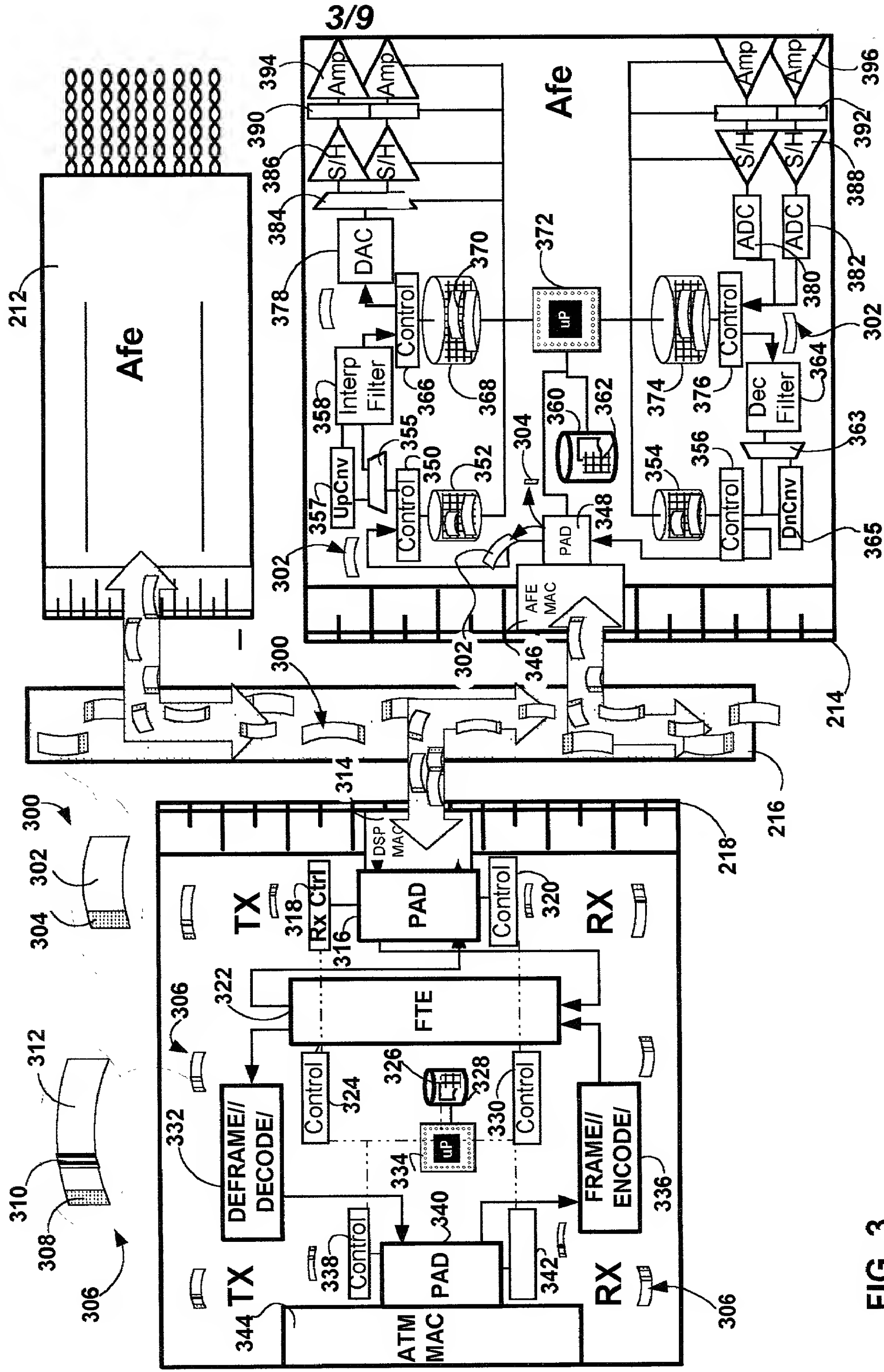


FIG. 3

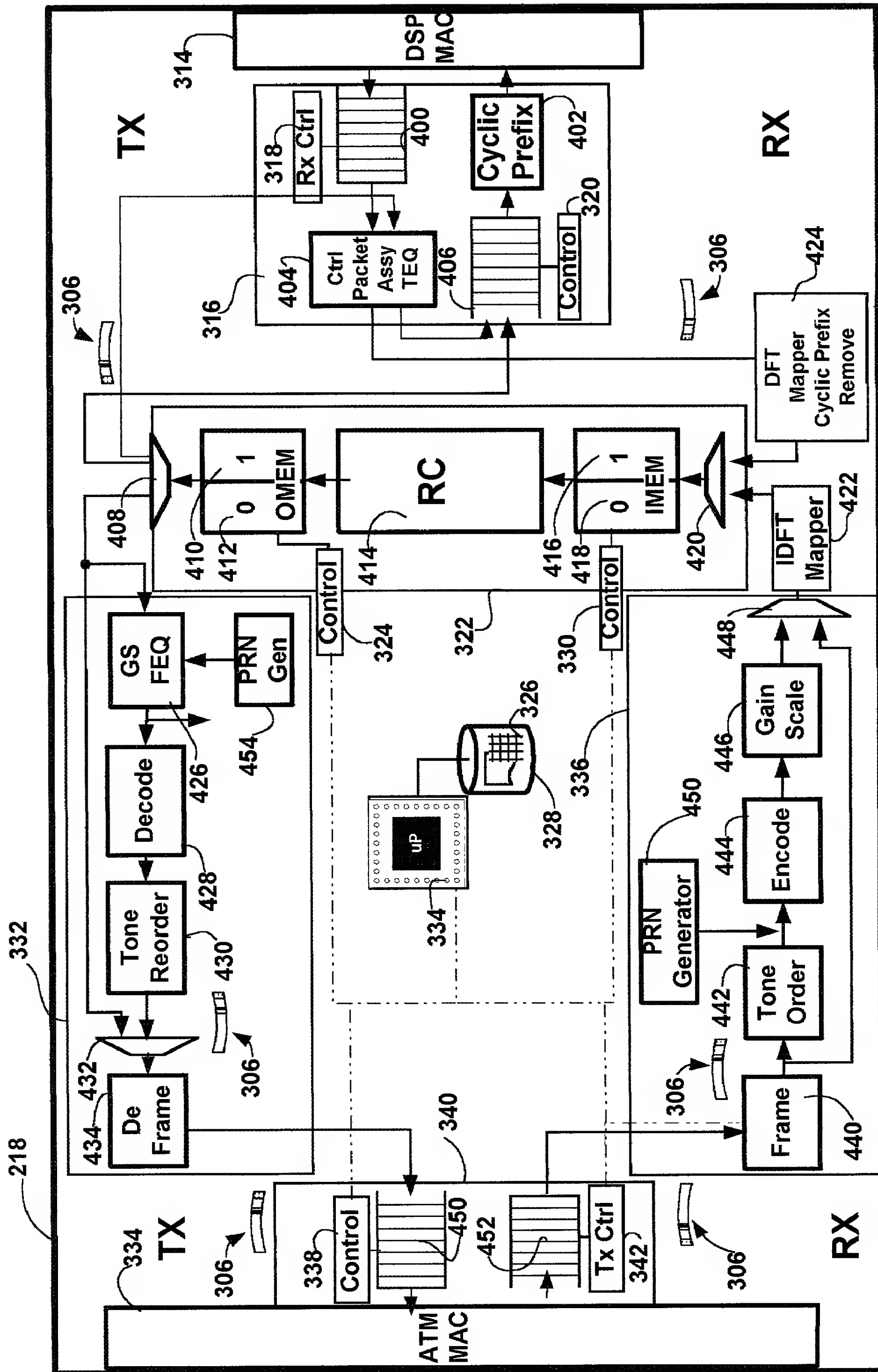
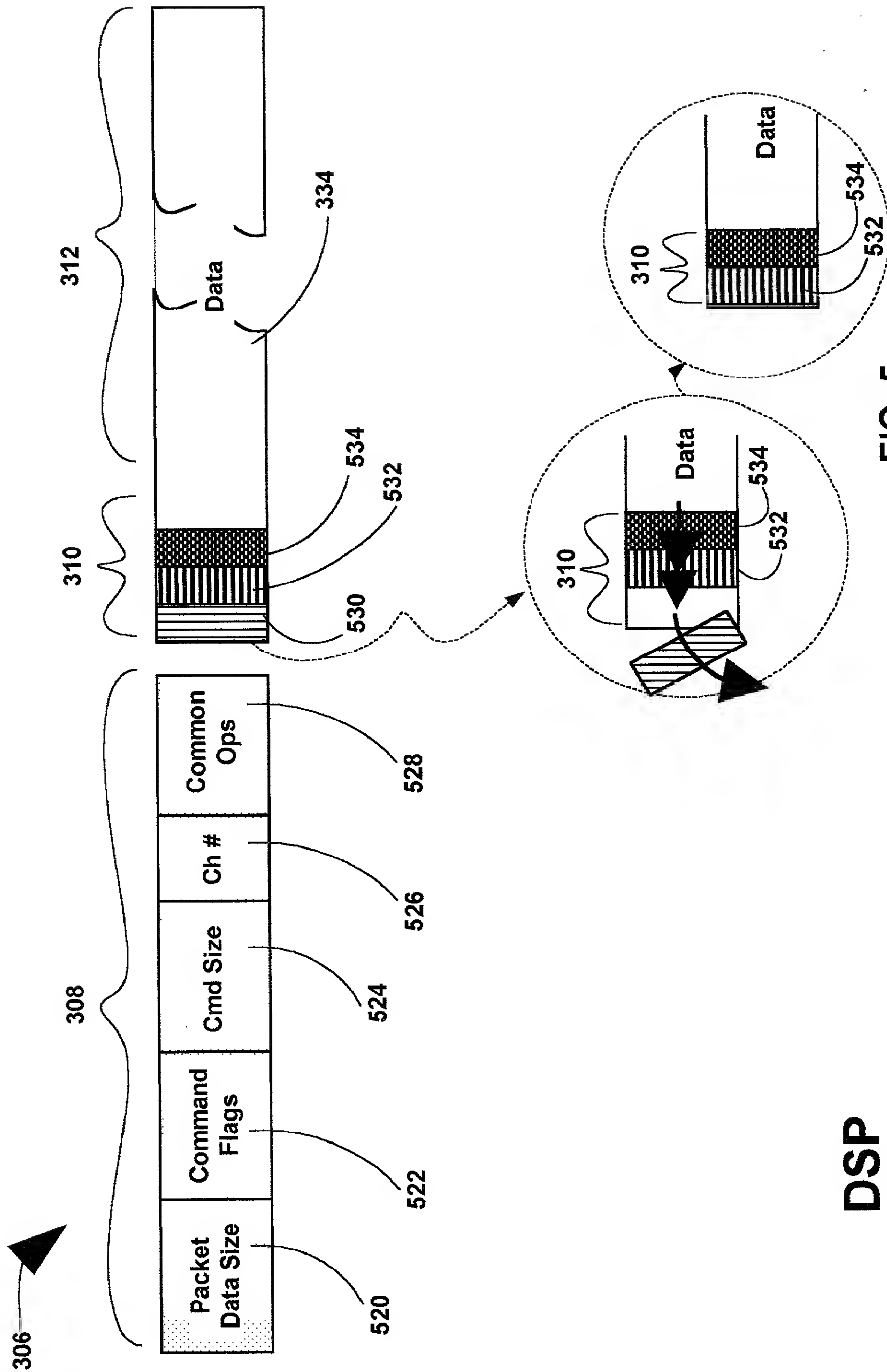


FIG. 4



DSP PACKETS

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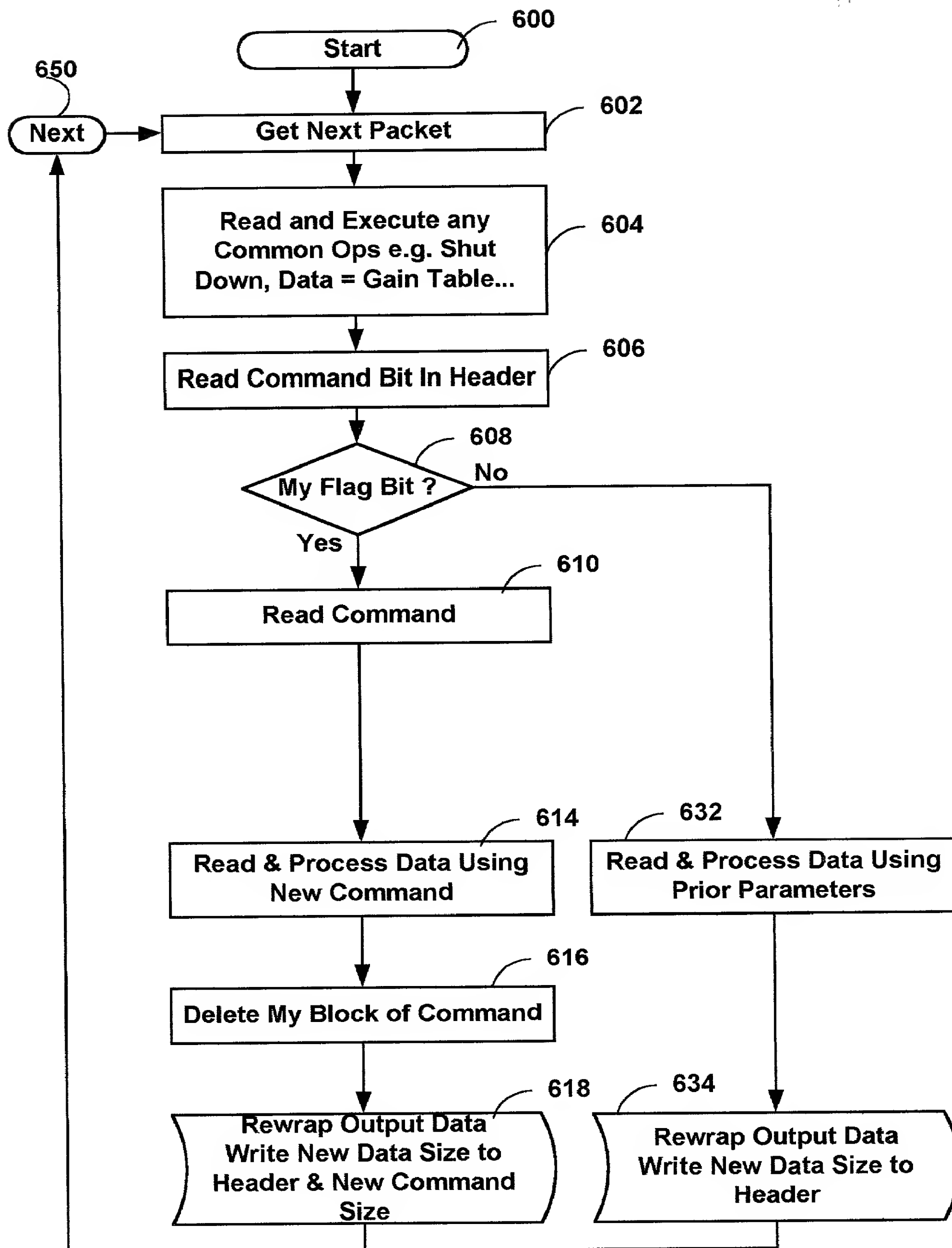
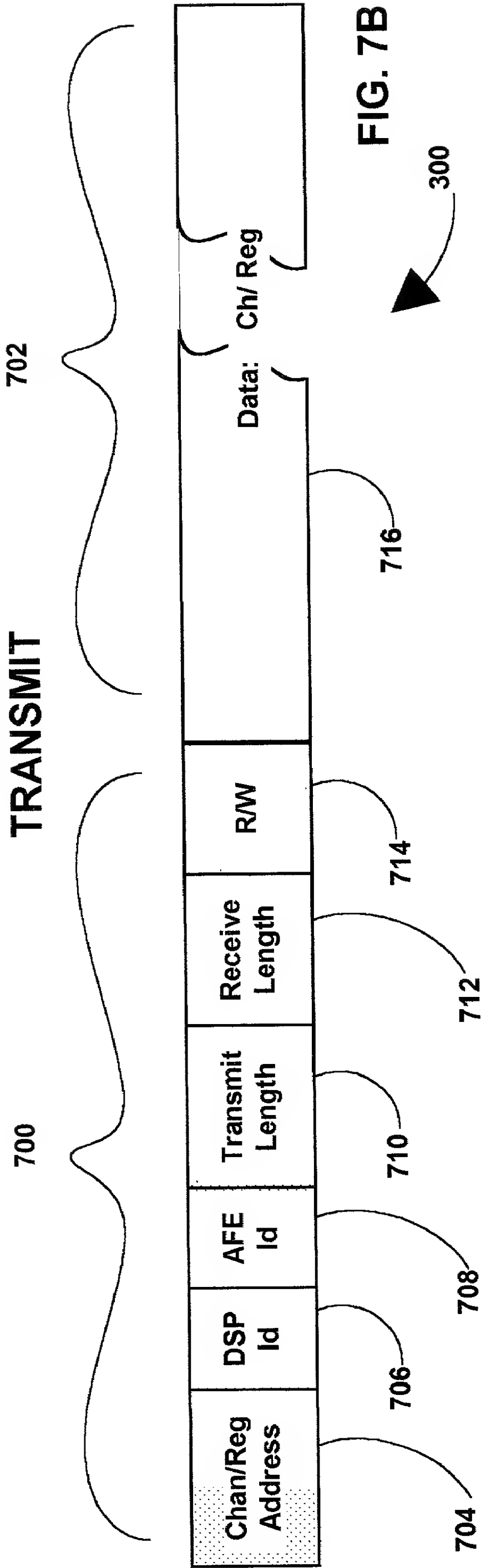
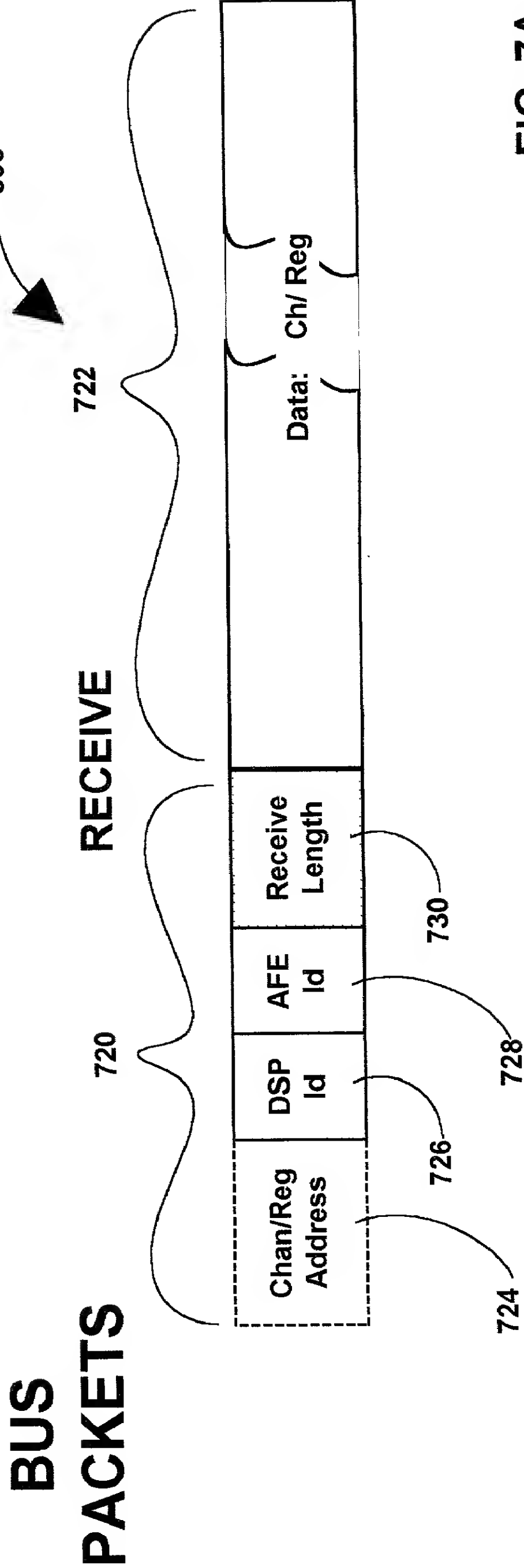


FIG. 6



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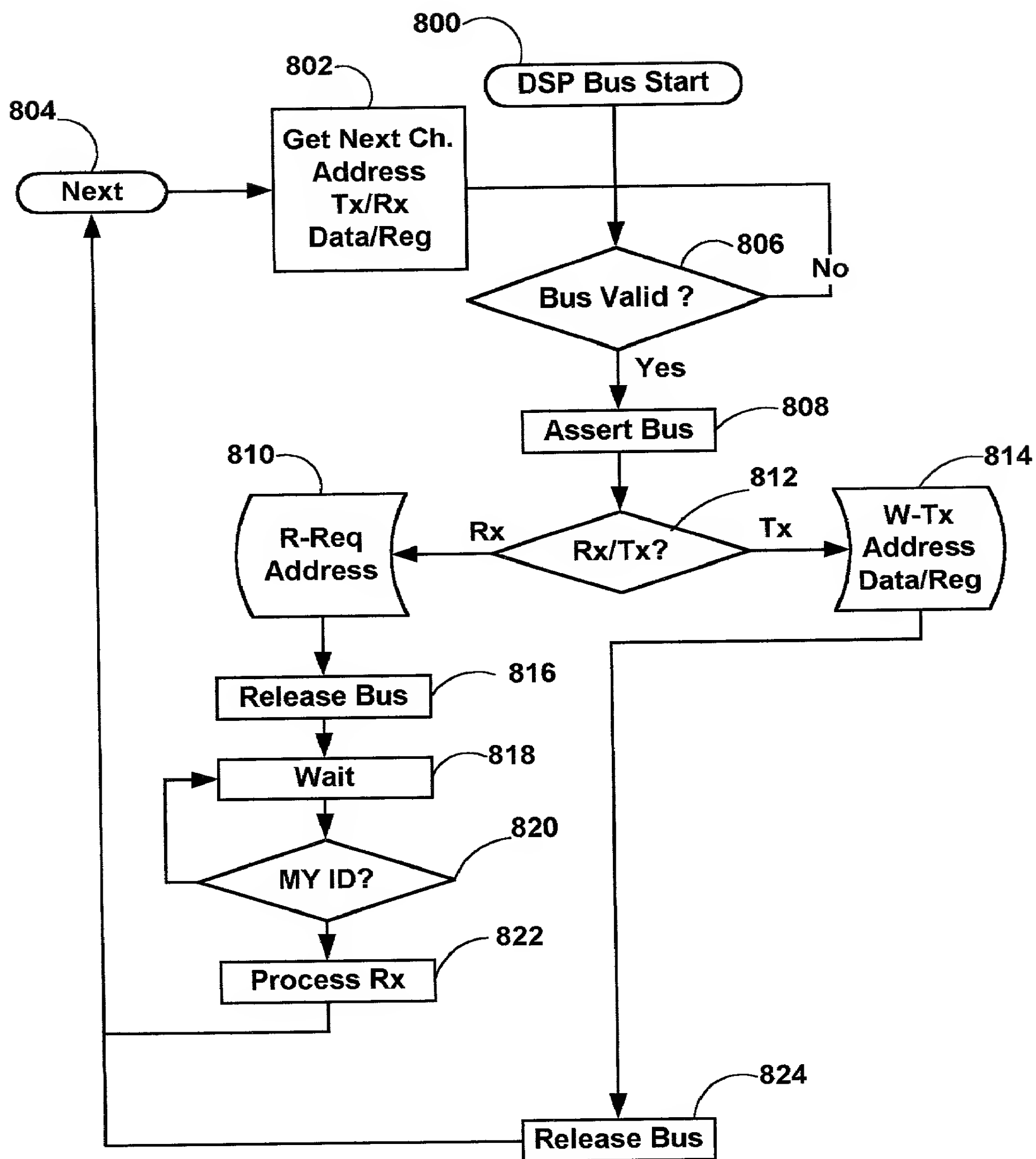


FIG. 8A

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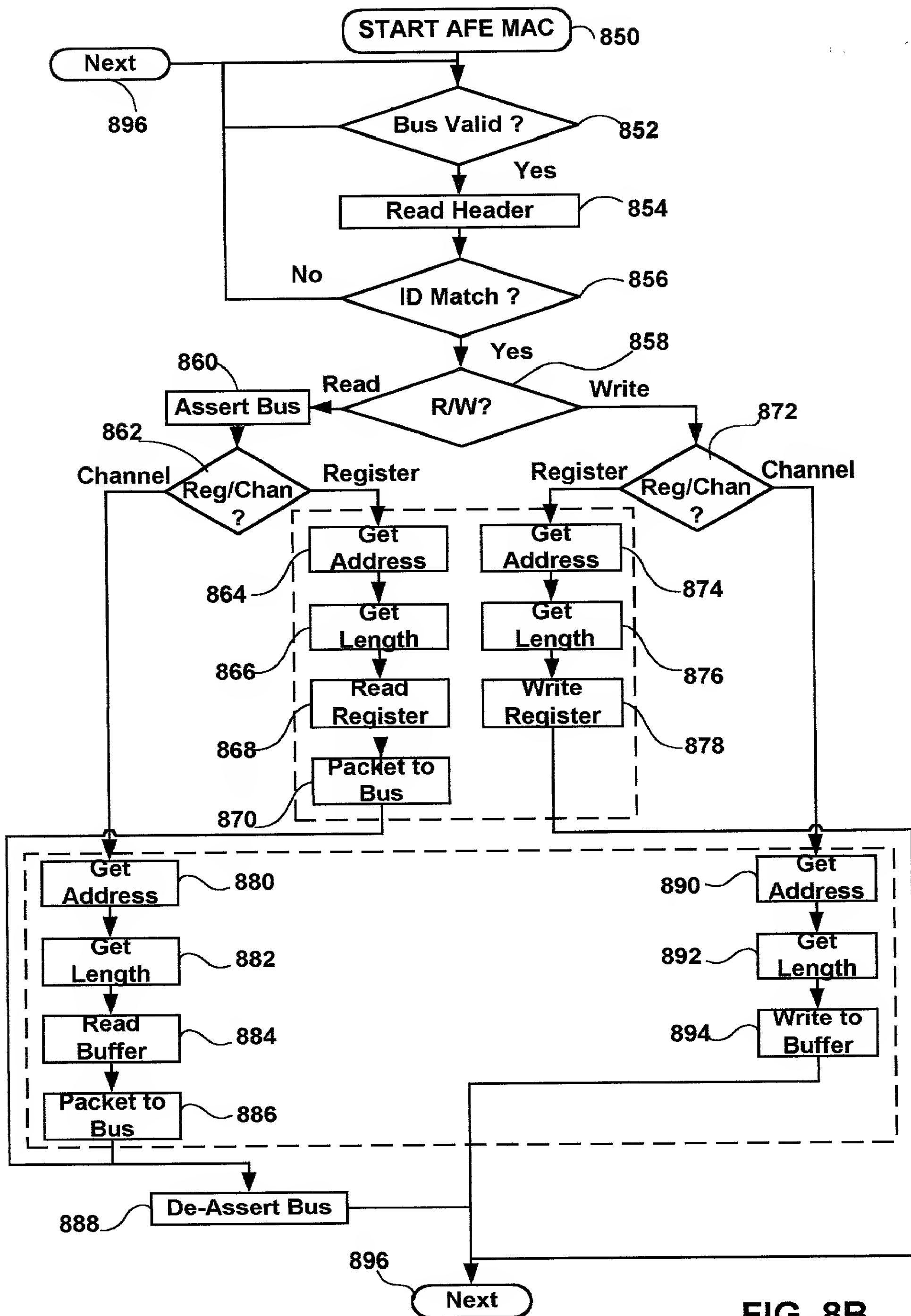


FIG. 8B